

JKCM(MOTOR) FUNCTIONAL SPECIFICATIONS

ITEM		SPECIFICATIONS					
INPUT		NPN Transistor					
OUTPUT		NPN Transistor (T) 30VDC 300mA					
OPERATION METHOD		Cyclic Operation					
PROGRAMMING LANGUAGE		Instruction list,ladder diagram					
PROGRAMMING STEP		3000 Step					
OPERATING SPEED		0.5 μS					
LATCH		Data Flash					
PROGRAM CAPACITY		4 KB					
MODEL		JKCM-16		JKCM-24		JKCM-32	
I/O POINTS	MAX POINTS	16 points		24 points		32 points	
	INPUT	8 points		14 points		18 points	
		8 Input		14 Input		18 Input	
	OUTPUT	X0 - X7		X0 - X7, X10 - X15		X8 - X7, X10 - X17,X20 - X21	
		8 points		10 points		14 points	
1 High Speed Pulse		7 Output	2 High Speed Pulse	8 Output	3 High Speed Pulse	11 Output	
	Y0	Y1 - Y7	Y0 - Y1	Y2 - Y7,Y10 - Y11	Y0 - Y2	Y3 - Y7,Y10 - Y15	
STATE RELAY (S)	32 POINTS	S0- S31					
AUXILIARY RELAY (M)	128 POINTS	M0- M127					
TIMER (T)	256 POINTS	T0-T199 : 100ms Non-Retentive					
		T200-T245 : 10ms Non-Retentive					
		T246-T249 : 1ms Retentive					
		T250-T255 : 100ms Retentive					
SPECIFICATION	100ms Timer : Setting time range 0.1-3276.7S						
	10ms Timer : Setting time range 0.01-327.67S						
	1ms Timer : Setting time range 0.001-32.767S						
COUNTER (C)	235 POINTS	16 bit general counter			32 bit general / Bi-direction counter		
		General	Latch Relay	Latch Relay	Special		
		C0 ~ C99	C100 ~ C199	C200 ~ C219	C220 ~ C234		
	SPECIFICATION	16 bit counter : Setting Value K0-32,767					
32 bit counter : Setting Value K-2147483648~ +2147483647							
DATA REGISTERS(D)	8256 POINTS	7600 General		D0 - D199 , D600 - D7999			
		400 Latch Relay		D200 - D599			
		256 Special		D8000 - D8255			
CODE PROTECTION		6bit ASCII					
SELF-DIAGNOSTIC		Power-0n self-test,watchdog timer,syntax check					
DIMENSION (L x W x H)		100 x 70 x 90 mm		110 x 115 x 84 mm		115 x 115 x 84 mm	
30 General Instruction:LD, LDI, AND, ANI, OR, ORI, INV, OUT, (OUTT, OUTC), SET, RST, ANB, ORB, LDP, LDF, ANDP, ANDF, ORP, ORF, PLS, PLF, MPS, MRD, MPP, RET, NOP, END,PLSY,PWM,MOV							
PLSY Pulse Y output	Format PLSY S1 S2 D	S1 Specify frequency K,Dn K as constant,setting range 2-32767 Dn as Data Register			S2 Specify pulse amount K,Dn K as constant,16 bit operation with setting range 0-32767 If "0" is specified for K,the PLSY instruction will continue generating pulses for as long as the instruction is energized Dn as Data Register		D Y Output Y0 ,Y1 ,Y2
PWM Pulse width modulation	Format PWM S1 S2 D	S1 Specify pulse width (msec) K,Dn K as constant,setting range 0-32767mS Dn as Data Register			S2 Specify cycle (msec) S1 ≤ S2 K,Dn K as constant,setting range 1-32767mS Dn as Data Register		D Y Output Y0 ,Y1 ,Y2
MOV Move Data	Format MOV S D Move Data from storage S to a new storage D	Kn,Dn S K as constant,setting range 0-32767mS Dn as Data Range			D Dn Dn as Data Register		